



REMARKS

Claims 1-2, 4-9, and 11-25 are pending. Claims 13-14 are rejected under 35 U.S.C. § 112, second paragraph. Claims 1-6 and 15-18 are rejected under 35 U.S.C. § 102(e) as being anticipated by Storm et al. (U.S. Pat. No. 6,175,561). Claims 7-9, 11, 14, 18-21, and 23-24 are rejected under 35 U.S.C. § 103(a). Claims 10, 12, and 22 are objected to as being dependent upon a rejected base claim. Claims 1, 4, 6, 11-15, and 25 are amended. Claims 3 and 10 are cancelled without prejudice.

The drawings are objected to because Figure 1 should be designated (Prior Art) and Figure 5 should include labels "MASK 2" "MASK 3" and "MASK 4". Replacement drawing sheets are included with appropriate corrections. 

Claims 13-14 are amended as suggested by Examiner to overcome the instant rejection under 35 U.S.C. § 112, second paragraph. 

Independent claims 1, 15, and 25 are amended to include limitations from claim 10. Claim 10 is cancelled without prejudice. Claim 1, as amended, recites "A method for shifting the phase of a pseudorandom noise (PN) code, the method comprising: accepting a PN code with a first phase; determining a first time interval; *selecting a plurality of phase-shifting masks in response to the first time interval; shifting the PN code first phase with each phase-shifting mask from the plurality of selected phase-shifting masks; and generating a PN code with a second phase, offset by the first time interval from the PN code first phase.*" (emphasis added).

Claim 15, as amended, recites "A receiver, comprising: a memory having a port to supply a plurality of phase-shifting masks; *an application means to determine a first time interval, the application means cross-referencing the first time interval to the plurality of phase-shifting masks*, the application means having an output connected to the memory port to request the plurality of phase-shifting masks; and a pseudorandom noise (PN) code generator having a first input connected to the memory to accept the plurality of phase-shifting masks, *the PN code*

*generator offsetting a PN code with each phase-shifting mask of the plurality of phase-shifting masks, the PN code generator having an output to supply the PN code with a second phase, offset from the PN code first phase.” (emphasis added).*

Claim 25, as amended, recites “A method for conserving power in a slotted mode of operation, the method comprising: storing a plurality of phase-shifting masks; generating a synchronized pseudorandom noise (PN) code to despread transmissions; accepting a slotted mode sleep second time interval from a plurality of second time intervals; beginning the sleep mode at a first phase of the PN code; ending the sleep interval; determining the first time interval between the beginning and the end of the sleep interval; and selecting a plurality of phase-shifting masks from storage in response to the first time interval; offsetting the PN code first phase with each phase-shifting mask from the plurality of selected phase-shifting masks; generating the PN code with a second phase; and resynchronizing the generated PN code to despread transmissions.” (emphasis added).

Applicants fail to find the foregoing emphasized limitations in any of the cited references, taken alone or in combination. Thus, applicants respectfully submit that independent claims 1, 15, and 25, as amended, are patentable under 35 U.S.C. § 102(e).

Applicants has acknowledge the rejections of depending claims 7-9, 11, 14, 18-21, and 23-24 under 35 U.S.C. § 103(a), but consider them moot in view of the present amendment as discussed.

In view of the foregoing, applicants respectfully request reconsideration and allowance of claims 1-2, 4-9, and 11-25. If the Examiner finds any issue that is unresolved, please call applicants' attorney by dialing the telephone number printed below.

Respectfully submitted,



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